REMARKS

Claim 25 stands rejected under 35 USC §112, second paragraph. Claims 19-22, 25-27, 30-31 stand rejected under 35 USC §102(b) as being anticipated by Yilmaz et al U.S. Patent 5,715,156.

Claims 23, 24, 28 and 29 stand rejected under 35 USC §103(a) over Yilmaz et al. Claims 19-31 stand rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-18 of U. S. Patent No. 6,700,808.

Applicant appreciates the Examiner's time for granting an Examiner's Interview of August 26, 2004. Consistent with the discussion with the Examiner, Claims 19-29 are removed from the present application to simplify and advance the present application toward issuance.

With regards to independent Claim 30, Applicant has amended this claim to recite that the removable program module selectively establishes a resistor as part of a feedback loop of the third circuit. Support for this limitation is found in Applicant's specification in Figure 1(a), and as discussed in Applicant's specification on page 9 line 24 – page 10 line 2. Applicant respectfully submits that Yilmaz et al U.S. Patent 5,715,156 fails to teach or suggest a removable program module selectively establishing a resistor as part of a feedback loop of a third circuit.

Applicant is also providing herewith a supplemental Information Disclosure Statement listing recently identified references. Applicant respectfully requests the Examiner to consider these references and indicate same in this application. A fee for submitting this IDS after a First Office Action is included herewith.

Applicant is also providing herewith a Terminal Disclaimer to overcome the judicially created doctrine of obviousness-type double patenting rejection.

Applicant respectfully submits that pending Claims 30-33 are allowable over the cited prior art, and a notice to this effect is respectfully requested.

Respectfully Submitted,

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